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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/757,250
Filing Date	01-14-2004
First Named Inventor	Levine et al.
Examiner Name	Kendall, Chuck O.
Art Unit	2192
Attorney Docket No.	AUS920030488US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/C.K./ AA	5,797,019	08-18-1998	Levine et al.	
/C.K./ AB	6,549,998 B1	04-15-2003	Pekarich et al.	
/C.K./ AC	2003/0101367 A1	05-29-2003	Bartfal et al.	
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/C.K./ AF	6,925,424 B2	08-02-2005	Jones et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
/C.K./ AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
/C.K./ AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, IPCOM000012728D, pp. 1-2
/C.K./ AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

/Chuck Kendall/

Date Considered:

05/21/2008

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